L Number	Hits	Search Text	DB	Time stamp
1	134	"semiconductor chip" and "memory chip" and "peripheral circuit" and dedicat$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/20 14:46
2	2	("semiconductor chip" and "memory chip" and "peripheral circuit" and dedicat$3) and "first chip" and "second chip"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/20 14:48
3	5	("semiconductor chip" and "memory chip" and "peripheral circuit" and dedicat$3) and "design rule"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/20 14:48
-	2	("5969383").PN.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/11 15:31
-	2	("5969383").PN.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/10 13:13
-	4	(("6741486") or ("6594169")).PN.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/20 07:35
-	4944	"first memory" and "second memory" and peripheral	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/20 07:37
-	66	"first memory" same "second memory" same "peripheral circuit"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/20 09:57
-	5	"first memory" same "second memory" same peripheral same dedicate$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/20 10:37
-	1		USPAT	2004/07/20 10:03
-	1		USPAT	2004/07/20 10:05
-	1		USPAT	2004/07/20 10:05